

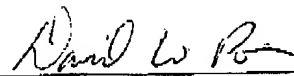
REMARKS

The applicants appreciate the Examiner's thorough examination of the Application and request reexamination and reconsideration of the Application in view of the preceding proposed amendment. Applicants' representative appreciates the teleconference with the Examiner on September 14, 2004.

The subject invention results from the realization that the underlying problem in degradation of common mode signal rejection with frequency is really due to package capacitance which is the parasitic capacitance developed between a power supply pin and a gain resistor pin and thus the common mode rejection could be dramatically improved by decreasing the effect of the package capacitance by spacing each power supply pin an equal distance from a respective one of the gain resistor pins to balance the package capacitance between those pins. See the subject application at page 2, line 22 to page 3, line 5 and Applicant's Response dated July 8, 2004.

If for any reason it appears that a telephone conference with counsel would help advance prosecution, please telephone the undersigned, or his associates, collect in Waltham, Massachusetts, at (781) 890-5678.

Respectfully submitted,


David W. Poirier
Reg. No. 43,007

AD-230J
DWP:wj